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THE ORMATION DISCLOSURE APPLICANT

(Use as many sheets as necessary)

2 Sheet 1 of

Application Number	10/675,549	
Filing Date	9/30/2003	
First Named Inventor	Jong-Hoon Oh	
Group Art Unit	2824	
Examiner Name	Van Thu Nguyen	
Attorney Docket Number	INFN/SZ0021	
Submission Date	July 27, 2005	

U.S. PATENT DOCUMENTS						
Examiner	Cite No.1	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where	
Initials*	No.	Number-Kind Code ^{2 (If known)}	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear	
	A1	US-				
	A2	US-				
	А3	US-				
	A4	US-				
	A5.	US-				
	A6	US-				
	A7	US-				
	A8	US-		<u> </u>		
	A9	US-				
	A10	US-				
	A11	US-				
	A12	US-				

		FOR	EIGN PATENT	DOCUMENTS		
Examiner	Cite	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines,	T ⁶ .
Initials*	No:1	Number-Kind Code ^{2 (f known)}	MM-DD-YYYY		Where Relevant Passages or Relevant Figures Appear	·
411	B1	EP 0 344 886 A1	12-06-1989	International Computers Limited		
	B2					
	В3					
	84					
	85					

Examiner **Date Considered**

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Substitute for form 1	449B/PTO		Application Number	10/675,549	
			Filing Date	9/30/2003	
INFORMAT	TION DISCLO	SURE	First Named Inventor	Jong-Hoon Oh	
STATEMENT BY APPLICANT		Group Art Unit	2824		
			Examiner Name	Van Thu Nguyen	
(Use as m	e as many sheets as necessary) Attorney Docket Number		INFN/SZ0021		
Sheet 2	0	f 2	Submission Date	July 27, 2005	フ

•		NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No. Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			
VIIN	C1	PCT International Search Report and Written Opinion dated June 8, 2005.		
	C2	General DDR SDRAM Functionality, Micron Technical Note TN 46-05, Micron Technology, Inc., July 2001, pp. 1-11.		
	C3	KDM432D2131 – 64Mbit DDR SGRAM, 512K x 32Bit x 4 Banks, Double Data Rate Synchronous Graphic RAM with Bi-directional Data Strobe, Samsung Electronics Target Specification, Revision 0.3, May 1999, pp. 1-49.		
MIN	C4	C. H. KIM et al., A 64-Mbit, 640-MByte/s Bidirectional Data Strobed, Double-Data-Rate SDRAM with a 40-mW DLL for a 256-Mbyte Memory System, IEEE Journal of Solid-State Circuits, Vol. 33, No. 11, pp. 1703-1709, Nov. 1998.		
	C5			
	C6			
	C7			
	C8			
	C9			
	C10			
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